

1 FACE-TO-FACE MULTI-CHIP FLIP-CHIP PACKAGE

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3 FIELD OF THE INVENTION

4 The present invention is relating to a multi-chip flip-chip package, particularly to a
5 face-to-face multi-chip flip-chip package.

6 BACKGROUND OF THE INVENTION

7 An improved multi-chip flip-chip package had been disclosed in US Patent No.
8 6,084,308 entitled "chip-on-chip integrated circuit package and method for making the
9 same". A face-to-face flip chip assembly comprises a first chip and a second chip, and
10 mounted on a package substrate. The package substrate has a cavity so as to
11 accommodate a second chip which is face-to-face flip-chip mounted on the first chip.
12 Also an insulation body is formed between the first chip and the second chip by capillary
13 filling method. But a problem is that air pocket happens easily in the insulation body
14 due to different capillary flowing speed. Please referring to Fig.1, a common
15 face-to-face multi-chip flip-chip package is disclosed. A second chip 30 that is smaller
16 than a first chip 20 in size is flip-chip mounted onto the first chip 20, and furthermore the
17 second chip 30 is placed in the opening 11 of the package substrate 10. When an
18 underfilling material 40 is dispensed, the flow speed of the underfilling material 40
19 between square lateral walls of the opening 11 and the second chip 30 is much faster than
20 that under active surface of the second chip 30 with the bumps under capillary effect.
21 When the underfilling material 40 had flowed fast between the opening 11 and the second
22 chip 30 and closed up the perimeters of the second chip 30, the underfilling material 40
23 among the bumps under the second chip 30 stop capillary flow to cause air pocket
24 between the second chip 30 and the first chip 20 resulting in serious dispensing failure.
25 Normally, the opening 11 of the common substrate 10 is rectangle so that the stress
26 would be concentrated at the corners of the opening 11 to decrease the reliability of the
27 product.

SUMMARY

A primary object of the present invention is to provide a face-to-face multi-chip flip-chip package to solve the problem of harmful capillary flow of an underfilling material. The face-to-face multi-chip flip-chip package includes a package substrate having a concave wall to define a chip accommodation space. The concave wall is not parallel to an adjacent side surface of second chip inside the chip accommodation space. The concave wall is designed to reduce the capillary flow speed of underfilling material between the side surface of second chip and the chip accommodation space of the package substrate so as to avoid forming air pocket between second chip and first chip while dispensing underfilling material.

11 A secondary object of the present invention is to provide a face-to-face multi-chip
12 flip-chip package. The face-to-face multi-chip flip-chip package includes a package
13 substrate having a chip accommodation space defined by a concave wall for diminishing
14 stress concentration of the package substrate.

According to the present invention, a face-to-face multi-chip flip-chip package comprises a package substrate having a top surface, a bottom surface and a concave wall between the top surface and the bottom surface. The concave wall defines a chip accommodation space, such as an opening or a cavity on the bottom surface. Preferably, the chip accommodation space is an opening in the shape of circular or elliptic shape to avoid the problem of stress concentration. A first chip, such as logic chip or other big size of chip, is mounted to the package substrate. The first chip has a first active surface with first bumps and a first back surface. The first chip is flip-chip mounted to the bottom surface of the package substrate. At least a second chip, such as a memory chip with a small size or other small size of chip, is flip-chip mounted to the first chip. The second chip has a second active surface, a second back surface and a plurality of side surfaces between the second active surface and the second back surface. Preferably, the second chip is disposed inside the chip accommodation space. By means of face-to-face

1 flip-chip mounting technique, bumps are formed between the second active surface of the
2 second chip and the first active surface of the first chip. An underfilling material is
3 formed between the first chip and the second chip by dispensing method. The concave
4 wall has a progressive distance (not parallel) from adjacent side surfaces of the second
5 chip, that is the four corners of the second chip are closer to the concave wall to influence
6 flow resistant of underfilling material, so that the capillary flow speed of the underfilling
7 material between the side surfaces of the second chip and the concave wall will be
8 reduced.

DESCRIPTION OF THE DRAWINGS

10 Fig.1 is a top view of a conventional face-to-face multi-chip flip-chip package when
11 dispensing an underfilling material.

12 Fig.2 is a cross-sectional view of a face-to-face multi-chip flip-chip package in
13 accordance with the present invention.

14 Fig.3 is a three-dimension view of a face-to-face multi-chip-flip-chip package
15 during dispensing an underfilling material in accordance with the present invention.

16 Fig.4 is a top view of a face-to-face multi-chip flip-chip package in condition of
17 flow of a dispensed underfilling material in accordance with the present invention.

DETAILED DESCRIPTION OF THE PRESENT INVENTION

19 Referring to the drawings attached, the present invention will be described by means
20 of the embodiments below.

21 According to an embodiment of the present invention, referring to Fig.2, a
22 face-to-face multi-chip flip-chip package 100 mainly comprises a package substrate 110,
23 a first chip 120, at least a second chip 130 and an underfilling material 140.

24 The package substrate 110 is a circuit board of chip carrier, such as printed circuit
25 board, ceramic circuit board or flexible circuit film. The package substrate 110 has a
26 top surface 112, a bottom surface 113 and a concave wall 114 between the top surface
27 112 and the bottom surface 113. The concave wall 114 defines a chip accommodation

1 space 111 which is a disc-like shape larger than the second chip 130 for containing the
2 second chip 130. The chip accommodation space 111 may be an opening or a cavity on
3 the bottom surface 113. In this embodiment, the chip accommodation space 111 is an
4 opening passing through the top surface 112 and the bottom surface 113. It is preferable
5 that the chip accommodation space 111 is an opening in shape of circular or elliptic shape.
6 The chip accommodation space 111 is smaller than the first chip 120 and larger than the
7 second chip 130.

8 The first chip 120 has a first active surface 121 and a first back surface 122
9 corresponding to the first active surface 121. The first chip 120 may be a logic chip,
10 microcontroller or memory chip, which is larger than the second chip 130 in size. In
11 this embodiment, the first chip 120 is larger than a chip accommodation space 111 of the
12 package substrate 110. A plurality of first bumps 123 are formed on perimeters of the
13 first active surface 121 of the first chip 120. The first active surface 121 of the first chip
14 120 faces toward the package substrate 110 and is flip-chip mounted to the bottom
15 surface 113 of the package substrate 110.

16 The second chip 130 is flip-chip mounted to the first chip 120 and smaller than the
17 first chip 120. Preferably, the second chip 130 is disposed inside the chip
18 accommodation space 111. The second chip 130 may be a memory or a driver chip.
19 The second chip 130 has a second active surface 131, a second back surface 132
20 corresponding to the second active surface 131 and a plurality of side surfaces 133
21 between the second active surface 131 and the second back surface 132. The second
22 bumps 134 are formed on the second active surface 131 for outer electrical connection.
23 Normally the side surfaces 133 of the second chip 130 are formed by dicing a wafer and
24 are vertical to the second active surface 131 and the second back surface 132. The
25 second active surface 131 of the second chip 130 faces to the first active surface 121 of
26 the first chip 120 by flip-chip mounting the second chip 130 to the first active surface 121
27 of the first chip 120, and the second bumps 134 are bonded between the second chip 130

1 and the first chip 120 in array. The side surfaces 133 of the second chip 130 have a
2 progressive distance from the concave wall 114 for influencing flow speed of the
3 underfilling material 140 during dispensing.

4 The thermosetting underfilling material 140 sealing the bumps 123, 134 is formed
5 between the first chip 120 and the second chip 130 by dispensing method. Furthermore,
6 a plurality of solder balls 150 are formed on the top surface 112 or the bottom surface
7 113 of the package substrate 110 for surface mounting. In this embodiment, the solder
8 balls 150 are formed on the top surface 112 of the package substrate 110.

9 Referring to Fig. 3 and 4,during a packaging process the un-solidified underfilling
10 material 140 is dispensed into the interval between one of side surfaces 133 of the second
11 chip 130 and the concave wall 114 by dispensing equipment 50. The underfilling
12 material 140 flow outward due to capillary reaction. The concave wall 114 has a
13 progressive distance from the side surface 133, that is to say, not parallel to the near side
14 surface 133 of the second chip 130. The four corners of the second chip 130 are closer
15 to the concave wall 114 of the package substrate 110 so that there is a larger flow
16 resistance of the underfilling material 140 at the corners of the second chip 130 to enable
17 to lessen the capillary flow of the underfilling material 140 between the concave wall 114
18 and the near side surface 133 thereof to be reduced effectively. The capillary flow
19 speed of the underfilling material 140 between the concave wall 114 and the near side
20 surface 133 of the second chip 130 is slower than that between the second chip 130 and
21 the first chip 120 (as showed in FIG.4). Therefore, the underfilling material 140
22 certainly is filled in the space between the second chip 130 and the first chip 120 in order
23 to avoid forming air pocket between the second chip 130 and the first chip 120.
24 Accordingly, the face-to-face multi-chip flip-chip package 100 effectively solves against
25 air pocket formed by the underfilling material 140 in the face-to-face multi-chip flip-chip
26 package, and releases stress concentration because the chip accommodation space 111 is
27 an opening in shape of circular or elliptic shape.

1 The above description of embodiments of this invention is intended to be illustrated
2 and not limiting. Other embodiments of this invention will be obvious to those skilled
3 in the art in view of the above disclosure.

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